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**REMARKS**

This paper is filed in response to the Office action mailed on March 29, 2004. Claims 28, 29, and 30 are amended; as a result, claims 1-30 are pending in the above-identified patent application. Claims 28, 29, and 30 were not amended in response to an art rejection. Claims 28, 29, and 30 were originally dependent claim that have now been rewritten in independent form.

§102 Rejection of the Claims

Claims 1, 2, 7, 9-10, 13-16, 20-22, and 27 were rejected under 35 U.S.C. § 102(b) as being anticipated by Okuzono *et al.* (U.S. 5,268,932). Applicant respectfully traverses the rejection of claims 1, 2, 7, 9-10, 13-16, 20-22, and 27.

Claims 1 and 13 recite, "wherein each of the plurality of write reset signals has a latency with respect to the read reset signal of less than or equal to one clock cycle." Claims 20 and 27 recite, "the read reset signal having a latency with respect to each of the plurality of write reset signals of less than or equal to one clock cycle." In contrast, as noted in the Office action in paragraph 4[d], Okuzono *et al.*, at column 12, lines 8-14, states:

A transmission line interface circuit according to claim 1, further comprising a reset signal generating means for generating reset signals respectively resetting said plurality of first and second FIFO mans, wherein each of said reset signals is synchronized with said pre-determined one of the plurality of receiving clock signals.

Hence, Okuzono *et al.* fails to teach "wherein each of the plurality of write reset signals has a latency with respect to the read reset signal of less than or equal to one clock cycle" or "the read reset signal having a latency with respect to each of the plurality of write reset signals of less than or equal to one clock cycle," so Okuzono *et al.* fails to teach each of the elements of claims 1, 13, 20, and 27. Thus, the Office action fails to state a *prima facie* case of anticipation with respect to claims 1, 13, 20, and 27. Therefore, applicant requests withdrawal of the rejections and reconsideration and allowance of claims 1, 13, 20, and 27.

Claims 2, 7, and 9-10 are dependent on claim 1. Claims 14-16 are dependent on claim 13. Claims 21-22 are dependent on claim 20. For reasons analogous to those stated above and elements in the claims, applicant respectfully submits that the Office action fails to state a *prima facie* case of anticipation with respect to claims 2, 7, 9-10, 14-16, and 21-22. Therefore, applicant requests withdrawal of the rejections and reconsideration and allowance of claims 2, 7, 9-10, 14-16, and 21-22.

§103 Rejection of the Claims

Claims 3-6, 8, 11-12, and 17-19 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Okuzono *et al.* Applicant respectfully traverses the rejections of claims 3-6, 8, 11-12, and 17-19.

All the elements of claims 3-6, 8, 11-12, and 17-19 are not found in Okuzono *et al.*

With respect to claim 3, the Office action, in paragraph 12, states, "It would have been obvious to one of ordinary skill in the art that the transceiver be any device than transmits and receives high bit rate data, such as a processor, substantially as claimed."

With respect to claims 4 and 5, the Office action, in paragraph 13, states, "It would have been obvious to one of ordinary skill in the art that the phase difference could comprise 180 degrees, or have a skew of less than 90 degrees, or any other difference in phase, substantially as claimed."

With respect to claim 6, the Office action, in paragraph 13, states, "It would have been obvious to one of ordinary skill in the art that data transmitted at 500 MHz to 5 GHz would comprise high bet rate data, substantially as claimed."

With respect to claim 8, the Office action, in paragraph 14, states, "It would have been obvious to one ordinary skill in the art that the master clock could be of any rate, such as twice the frequency of the received clocks, substantially as claimed."

With respect to claims 11-12 and 17-19, the Office action states, "It would have been obvious to one of ordinary skill in the art that the receiver be any device that receives high be rate data, such as DRAM or SRAM memory, substantially as claimed."

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Since all the elements of claims 3-6, 8, 11-12, and 17-19 are not found in Okuzono *et al.*, which is the only applied reference, applicant assumes that the Examiner is taking official notice of the missing elements from an undisclosed source. Applicant respectfully objects to the taking of official notice, and pursuant to M.P.E.P. § 2144.03, applicant traverses the assertion of official notice and requests that the Examiner cite a reference that teaches the missing elements. If the Examiner cannot cite a reference that teaches the missing elements, applicant respectfully requests that the Examiner provide an affidavit that describes how the missing elements are present in the prior art. If the examiner cannot cite a reference or provide an affidavit, applicant requests withdrawal of the rejections and reconsideration and allowance of claims 3-6, 8, 11-12, and 17-19.

Allowable Subject Matter

Claims 28-30 were objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all limitations of the base claim and any intervening claims. Claims 28-30 have been rewritten in independent form including all limitations of the base claim and any intervening claims. Therefore, applicant respectfully submits that the objections to claims 28-30 have been obviated.

Allowed Claims

Claims 23-26 are allowed.

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone applicant's attorney at 612-371-2109 to facilitate prosecution of the application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743

Respectfully submitted,

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By their Representatives,

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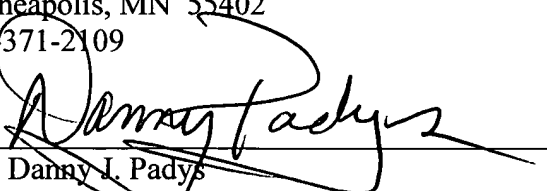
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Date

May 17, 2004

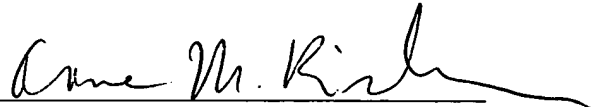
By

  
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CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 17 day of May, 2004.

**ANNE M. RICHARDS**

Name



Signature